AMENDMENT

In the Specification

Please amend the specification as follows:

Page 5, line 25 through page 6, line 6

Referring now to FIG. 2, an exemplary embodiment of the present invention is shown wherein a variety of bus interfaces are supported by a triple bus interface of the present invention. A bus interface system 200 may include a first bus interface 202, such as to a PCI bus, PCI-X bus, and the like, and a variety of additional bus interfaces, such as a SCSI interface 204, fiber interface 206, or other interface 208 as contemplated by a person of ordinary skill in the art. An arbiter 210 is provided for arbitration of commands. For example, the arbiter 210 may resolve competing demands for the interface. This may be accomplished by intercepting the commands from the first bus interface 202 and the variety of other bus interfaces provided 204, 206, and 208. Preferably, only commands are intercepted, since the other data, such as completion and data includes ID and tag data. However, in alternative embodiments a separate interface between the bus interfaces 204, 206, and 208 and the arbiter, may be enabled as provided by the present invention. Each interface including a command queuing interface, a data transfer interface, and a command interface.